

REMARKS

Present Status of the Application

The final Office action rejected claims 1-4, 6-8, 11-14, and 19 under 35 U.S.C. 103(a) as being unpatentable over Kang et al (US 2002/0063666 A1) in view of the instant Application's Admitted Prior Art (AAPA).

Applicant has amended claim 1 by incorporating dependent claim 4 and then canceling claim 4. The amendment is fully supported by the original specification of the present application without adding any new matter. Applicant has canceled claims 12-16 and claims 19-20. After entry of the foregoing amendments, claims 1-3, 6-8, and 11 remain pending in the present application, and reconsideration of those claims is respectfully requested.

Discussion of the claim rejections under 35 USC 103(a)

The Office action rejected claims 1-4, 6-8, 11-14, and 19 under 35 U.S.C. 103(a) as being unpatentable over Kang in view of AAPA.

In response thereto, Applicant has amended claim 1 to more clearly define the present application, upon which Applicant hereby traverses these rejections.

Specifically, Applicant respectfully submits that the present application, as set forth in claims 1-3, 6-8, and 11, is novel, non-obvious and patentable over Kang, AAPA, or any other cited references, taken alone or in combination. Therefore, Applicant respectfully submits that claims 1-3, 6-8, and 11 should be allowed.

With respect to the currently amended claim 1, it is recited in entirety below:

A color management structure for a panel display, comprising:

a display array unit;

a plurality of gate drivers;

a plurality of source drivers, said plurality of gate drivers and said plurality of source drivers driving said display array unit to display an image; and

a timing sequence control unit, said timing sequence control unit outputting a plurality of signals to said plurality of gate drivers and said plurality of source drivers to drive said display array unit, said timing sequence control unit **outputting a clock signal and a digital color management data to said plurality of source drivers, said timing sequence control unit comprising:**

a timing controller receiving a system input and **providing said clock signal; and**

a color management control block, coupled to said timing controller, **outputting said digital color management data and said clock signal to said plurality of source drivers**, said digital color management data being adjustable.

(Emphasis added)

The currently amended claim 1, which incorporates original claim 4, has at least three features not found in any of the cited references: **(1) the timing sequence control unit comprising the timing controller and the color management control block; (2)**

the timing controller providing the clock signal for the source drivers; and (3) the color management control block outputting the digital color management data and the clock signal to the source drivers.

1. Timing sequence control unit.

On page 7 of the final Office action, the Examiner states that the timing/gamma controller 142 in Fig.14 of Kang is equivalent to the timing sequence control unit of the original claim 1 of the present application. On page 9 of the final Office action, the Examiner states that the gamma controller 91 in Fig.9 of Kang is equivalent to not only the timing controller but also the color management control block of the original claim 4 of the present application.

However, in Kang, the gamma controller 91 in Fig.9 is not a constituent element of the timing/gamma controller 142 in Fig.14. Please refer to Kang paragraph [0051], and Kang Figs. 8 and 9. Rather than being a constituent element of the timing/gamma controller 142 in Fig.14, the gamma controller 91 in Fig. 9 is in fact a constituent element of the multi-mode gamma voltage generator 84 in Fig. 8. According to Fig. 8, the multi-mode gamma voltage generator 84 and the controller 82 are two distinct elements that have no interaction. According to Kang paragraphs [0050] and [0071], the controller 82 in Fig. 8 is distinct from the timing/gamma controller 142 in Fig. 14. Therefore, unlike Applicant's currently amended claim 1, Kang fails to disclose that the timing sequence control unit comprises the timing controller and the color management control block.

2. Timing controller.

On page 7 of the final Office action, the Examiner states that the column driver 143

in Fig. 14 of Kang is equivalent to the source drivers of the original claim 1 of the present application. But this is the third embodiment of Kang, paragraph [0069], an LCD comprising a timing/gamma controller. Then on page 9 of the final Office action, the Examiner states that the gamma controller 91 in Fig. 9 of Kang is equivalent to the timing controller of the original claim 4 of the present application, but the gamma controller of Fig. 9 is from the first embodiment of Kang, paragraph [0048], which comprises a multi-mode gamma voltage generator.

Currently amended claim 1 now includes claim 4, and so the timing controller of previously dependent claim 4 outputs (as it always has) to the same plurality of source drivers as claim 1. The examiner has the multi-mode gamma voltage generator from Fig. 9 serving as the timing/gamma controller from Fig. 14, but they are each from distinct embodiments, and not equivalent to Applicant's timing controller.

On page 7 of the final Office action, the Examiner first states that the clock in Fig.14 of Kang is equivalent to the clock signal of the original claim 1 of the present application. Then, on page 9 of the final Office action, the Examiner contradictorily states that the I²C clock in Fig.9 of Kang is equivalent to the clock signal of the original claims 1 and 4 of the present application. Because the I²C clock in Fig.9 and the clock in Fig.14 are two distinct signals, they can not be equivalent to the clock signal of the original claims 1 and 4 of the present application at the same time.

The I²C clock in Fig.9 of Kang is not provided to the column driver 143 in Fig.14, they are distinct and completely different embodiments, and the clock in Fig.14 is not provided by the gamma controller 91 in Fig.9. The gamma controller 91 in Fig.9 does not

provide any signal for the column driver 143 in Fig.14. Therefore, unlike the currently amended claim 1, Kang fails to disclose that the timing controller provides the clock signal for the source drivers.

On page 7 of the final Office action, the Examiner states that the column driver 143 in Fig.14 of Kang is equivalent to the source drivers of the original claim 1 of the present application. But again the gamma controller 91 in Fig.9 does not output any signal to the column driver 143 in Fig.14. In addition, the I²C clock in Fig.9 is not output to the column driver 143 in Fig.14. Therefore, unlike the currently amended claim 1, Kang fails to disclose that the timing controller provides the clock signal for the source drivers.

3. Color management control block.

Kang also fails to disclose that the color management control block outputs the digital color management data and the clock signal to the source drivers.

On page 9 of the final Office action, the Examiner states that the gamma controller 91 in Fig.9 of Kang is equivalent to the color management control block of the original claim 4 of the present application. Previously, on page 7 of the final Office action, the Examiner stated that either the γ data in Fig.14 or the I²C data in Fig.9 of Kang is equivalent to the digital color management data of the original claim 1 of the present application, but because the I²C data in Fig.9 and the γ data in Fig.14 are two distinct signals, they cannot be equivalent to the digital color management data of the original claim 1 of the present application at the same time. Neither is the clock in Fig.14 output by the gamma controller 91 in Fig.9, nor is the γ data in Fig.14 output by the gamma controller 91 in Fig.9.

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In sum, the currently amended claim 1 has at least three unique features, **(1) a timing sequence control unit comprising: (2) a timing controller providing a clock signal, and (3) a color management control block outputting digital color management data and said clock signal to a plurality of source drivers**, not found in any of the cited references, taken alone or in combination. Therefore, the currently amended claim 1 should be novel, non-obvious, and hence patentable. Accordingly, the currently amended claim 1 should be allowed.

Being dependent upon the allowable independent claim 1, the dependent claims 2-3, 6-8, and 11 should also be allowed.

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CONCLUSION

For at least the foregoing reasons, it is believed that all the pending claims 1-3, 6-8, and 11 of the present application are in proper condition for allowance. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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